

10/065,475

**IN THE CLAIMS:**

Please amend the claims as follows:

1. (Currently Amended) A driver circuit comprising:  
a plurality of parallel resistors, wherein a total impedance of all said resistors always  
equals an impedance of an associated transmission line; ~~and~~  
a switch adapted to combine said resistors to control an output level of said driver; ~~and~~  
balancing resistors connected to said switch, wherein said balancing resistors are sized to  
balance the resistance within said driver circuit.
  
8. (Currently Amended) A driver circuit connected to a transmission line, said driver circuit  
comprising:  
a plurality of parallel resistors, wherein a total impedance of all said resistors always  
equals an impedance of said transmission line; ~~and~~  
a plurality of switches, each connected to one of said resistors, said switches being  
adapted to combine said resistors to control an output level of said driver; ~~and~~  
balancing resistors connected to said switches, wherein said balancing resistors are sized  
to balance the resistance within said driver circuit.
  
15. (Currently Amended) A driver circuit connected to a transmission line, said driver circuit  
comprising:  
a plurality of parallel resistive segments, wherein a total impedance of all said resistive  
segments always equals an impedance of said transmission line,  
wherein each of said resistive segments includes two resistors and switches connected to  
said resistive segments, ~~and~~  
wherein said switches are adapted to combine said resistors to control an output level of  
said driver; ~~and~~  
balancing resistors connected to said switches, wherein said balancing resistors are sized  
to balance the resistance within said driver circuit.